

# 4A, 40V Schottky Barrier Surface Mount Rectifier

#### **FEATURES**

- AEC-Q101 qualified
- Low power loss, high efficiency
- Low profile package
- Ideal for automated placement
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

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- Low voltage, high freq. inverter
- DC/DC converter
- Freewheeling diodes
- Reverse battery protection
- Car lighting

#### **MECHANICAL DATA**

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.110g (approximately)

| KEY PARAMETERS     |            |       |  |  |  |
|--------------------|------------|-------|--|--|--|
| PARAMETER          | VALUE      | UNIT  |  |  |  |
| l <sub>F</sub>     | 4          | Α     |  |  |  |
| $V_{RRM}$          | 40         | V     |  |  |  |
| I <sub>FSM</sub>   | 100        | Α     |  |  |  |
| T <sub>J</sub> MAX | 150        | °C    |  |  |  |
| Package DO-214AA   |            | (SMB) |  |  |  |
| Configuration      | Single die |       |  |  |  |









DO-214AA(SMB)



| ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)            |                     |             |      |  |  |
|--|---------------------|-------------|------|--|--|
| PARAMETER  | SYMBOL              | SSB44H      | UNIT |  |  |
| Marking code on the device   |                     | SSB44       |      |  |  |
| Repetitive peak reverse voltage  | V <sub>RRM</sub>    | 40          | V    |  |  |
| Reverse voltage, total rms value   | V <sub>R(RMS)</sub> | 28          | V    |  |  |
| Forward current  | l <sub>F</sub>      | 4           | А    |  |  |
| Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load | I <sub>FSM</sub>    | 100         | А    |  |  |
| Junction temperature   | TJ                  | -55 to +150 | °C   |  |  |
| Storage temperature  | T <sub>STG</sub>    | -55 to +150 | °C   |  |  |



# Taiwan Semiconductor

| THERMAL PERFORMANCE                    |                  |     |      |  |
|--|------------------|-----|------|--|
| PARAMETER                              | SYMBOL           | TYP | UNIT |  |
| Junction-to-lead thermal resistance    | R <sub>OJL</sub> | 23  | °C/W |  |
| Junction-to-ambient thermal resistance | Reja             | 82  | °C/W |  |
| Junction-to-case thermal resistance    | Rejc             | 24  | °C/W |  |

Thermal Performance Note: Units mounted on PCB (10mm x 10mm Cu pad test board)

| ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted) |   |                |      |      |      |
|--|---|----------------|------|------|------|
| PARAMETER  | CONDITIONS                                  | SYMBOL         | TYP  | MAX  | UNIT |
|  | I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C  |                | 0.40 | -    | V    |
| [  | I <sub>F</sub> = 4A, T <sub>J</sub> = 25°C  | V <sub>F</sub> | 0.46 | 0.50 | V    |
| Forward voltage <sup>(1)</sup>   | I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C |                | 0.31 | -    | V    |
|  | I <sub>F</sub> = 4A, T <sub>J</sub> = 125°C |                | 0.41 | 0.45 | V    |
|  | T <sub>J</sub> = 25°C                       |                | -    | 200  | μA   |
| Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>                    | T <sub>J</sub> = 125°C                      | I <sub>R</sub> | -    | 40   | mA   |
| Junction capacitance   | 1MHz, V <sub>R</sub> = 4.0V                 | CJ             | 235  | -    | pF   |

# Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

| ORDERING INFORMATION |                |                     |  |  |  |
|----------------------|----------------|---------------------|--|--|--|
| ORDERING CODE        | PACKAGE        | PACKING             |  |  |  |
| SSB44H               | DO-214AA (SMB) | 3,000 / Tape & Reel |  |  |  |



AVERAGE FORWARD CURRENT (A)

## **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

**Fig.1 Forward Current Derating Curve** 

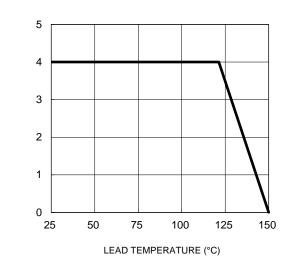


Fig.3 Typical Reverse Characteristics

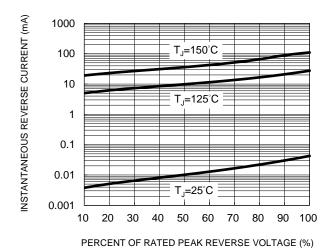


Fig.5 Typical Forward Power Dissipation vs. Forward Current

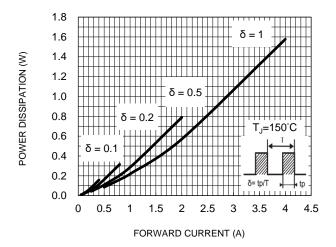


Fig.2 Typical Junction Capacitance

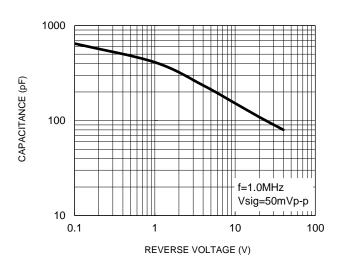
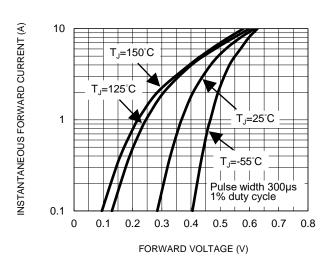


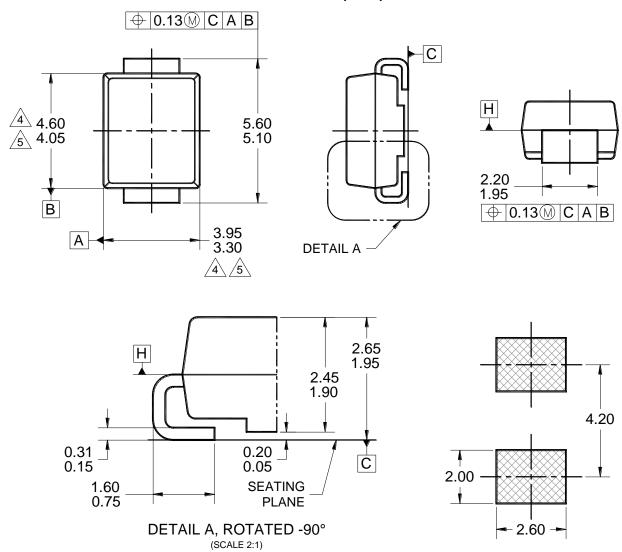
Fig.4 Typical Forward Characteristics

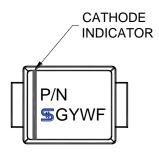




## **PACKAGE OUTLINE DIMENSIONS**

## **DO-214AA (SMB)**





## MARKING DIAGRAM

P/N = MARKING CODE

G = GREEN COMPOUND

YW = DATE CODE

F = FACTORY CODE

#### NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.

SUGGESTED PAD LAYOUT

- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-214, VARIATION AA, ISSUE D.
- MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
- MOLDED PLASTIC BODY LATERAL DIMENSIONS TO BE DETERMINED AT DATUM PLANE H.
- 6. DWG NO. REF: HQ2SD07-DO214SMB-035 REV A.



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